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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/667,545	09/22/2003	Timothy Chen	135432 (GECZ 2 00680)	9930
27885	7590	07/26/2005	EXAMINER	
FAY, SHARPE, FAGAN, MINNICH & MCKEE, LLP 1100 SUPERIOR AVENUE, SEVENTH FLOOR CLEVELAND, OH 44114			TRAN, THUY V	
			ART UNIT	PAPER NUMBER

2821

DATE MAILED: 07/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

This is a response to the Applicant's amendment submitted on May 9th, 2005. In virtue of this amendment:

- Claims 5, 14-15, and 19 are canceled; and thus,
- Claims 1-4, 6-13, 16-18, and 20 remain active in the instant application.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 17-18, and 20 are rejected under 35 U.S.C. 102(b) as being anticipated by Stack (U.S. Patent No. 6,222,322).

With respect to claim 17, Stack discloses, in Fig. 4, a lamp inverter starting circuit comprising (1) a switching portion [110] that includes first and second transistors [Q1, Q2], (2) a resonant load portion [T5, C9] for receiving a lamp [FL1], (3) a power factor correction circuit [106] (see col. 5, line 31) that delivers a bus voltage that ranges from 120 V to 230 VAC (which is within the claimed range of from about 120 V to about 300 V), and (4) a voltage dependent start-up portion [108] that delays firing (via diac [D9]; see col. 6, lines 45-67; and col. 7, lines 1-7) of the inverter until the bus voltage ramps up to a predetermined threshold (which is of the diac [D9]; see col. 6, lines 56-65).

With respect to claim 18, Stack further discloses, in Fig. 4, an AC line voltage that ranges from 120 V to 230 V (which is within the claimed range of 120 V to 280 V).

With respect to claim 20, Stack discloses, in Fig. 4, that the voltage dependent start-up portion [108] allows firing of the inverter circuit before the bus voltage reaches a steady state (see col. 5, lines 24-34).

Allowable Subject Matter

3. Claims 1-4, 6-13, and 16 are allowed.
4. The following is a statement of reasons for the indication of allowable subject matter:
Prior art fails to disclose or fairly suggest:
 - A lamp inverter starting circuit comprising an input portion that receives the bus voltage signal, wherein the bus voltage signal ranges up to 390 V, and a voltage controlled start-up portion that delays triggering of the inverter starting circuit based on the input bus voltage signal, in combination with the remaining claimed limitations as called for in independent claim 1 (claims 2-4 and 6-10 are allowed since they are dependent on claim 1); and
 - A method of firing a lamp comprising overcoming a breakdown voltage of a diac by ramping the bus voltage up to between about 300 V to about 500 V, turning the diac conductive when the charged capacitor reaches the diac breakdown voltage, in combination with the remaining claimed limitations as called for in independent claim 11 (claims 12-13 and 16 are allowed since they are dependent on claim 11).

Remarks and conclusion

5. Applicant's arguments, see pages 5 and 6 of the Amendment, filed on May 9th, 2005, with respect to independent claims 1 and 11 have been fully considered and are persuasive. Therefore, the rejections of these claims have been withdrawn, and as a result, they are now

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allowed along with the claims that are dependent on them. Specifically, claims 1-4, 6-13, and 16 are allowed.

6. For claim 17 and its dependents, the cited prior art Stack explicitly teaches a range of 120-230 VAC which is within the claimed range of 120 V to about 300 V. Therefore, claims 17-18 and 20 remain rejected.

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Inquiry

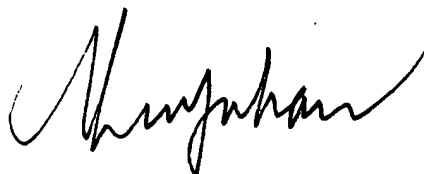
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

07/25/2005



THUY V. TRAN
PRIMARY EXAMINER

REPLACEMENT SHEET

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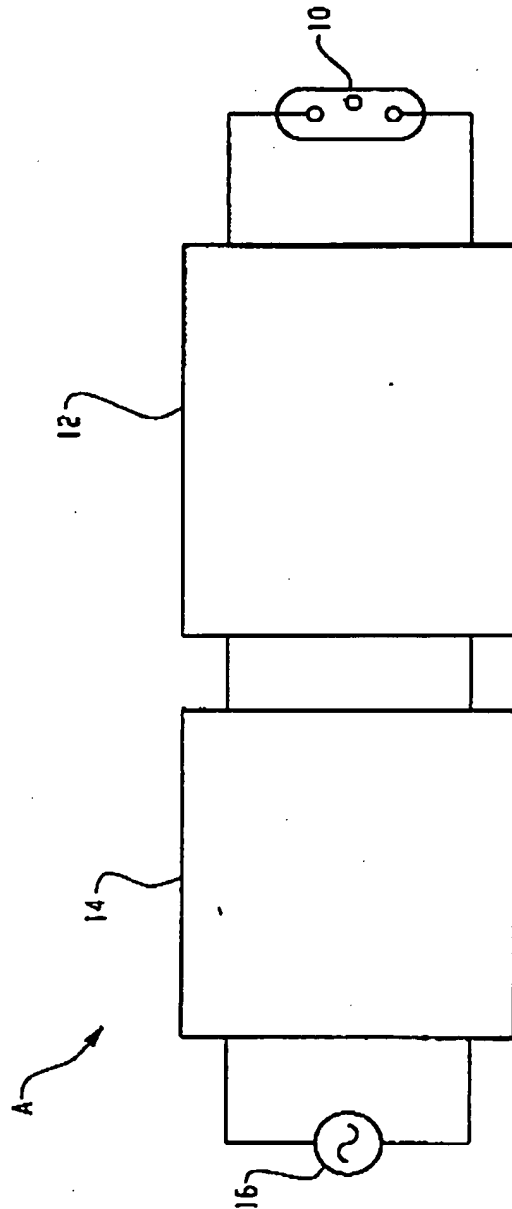


Fig. 1

REPLACEMENT SHEET

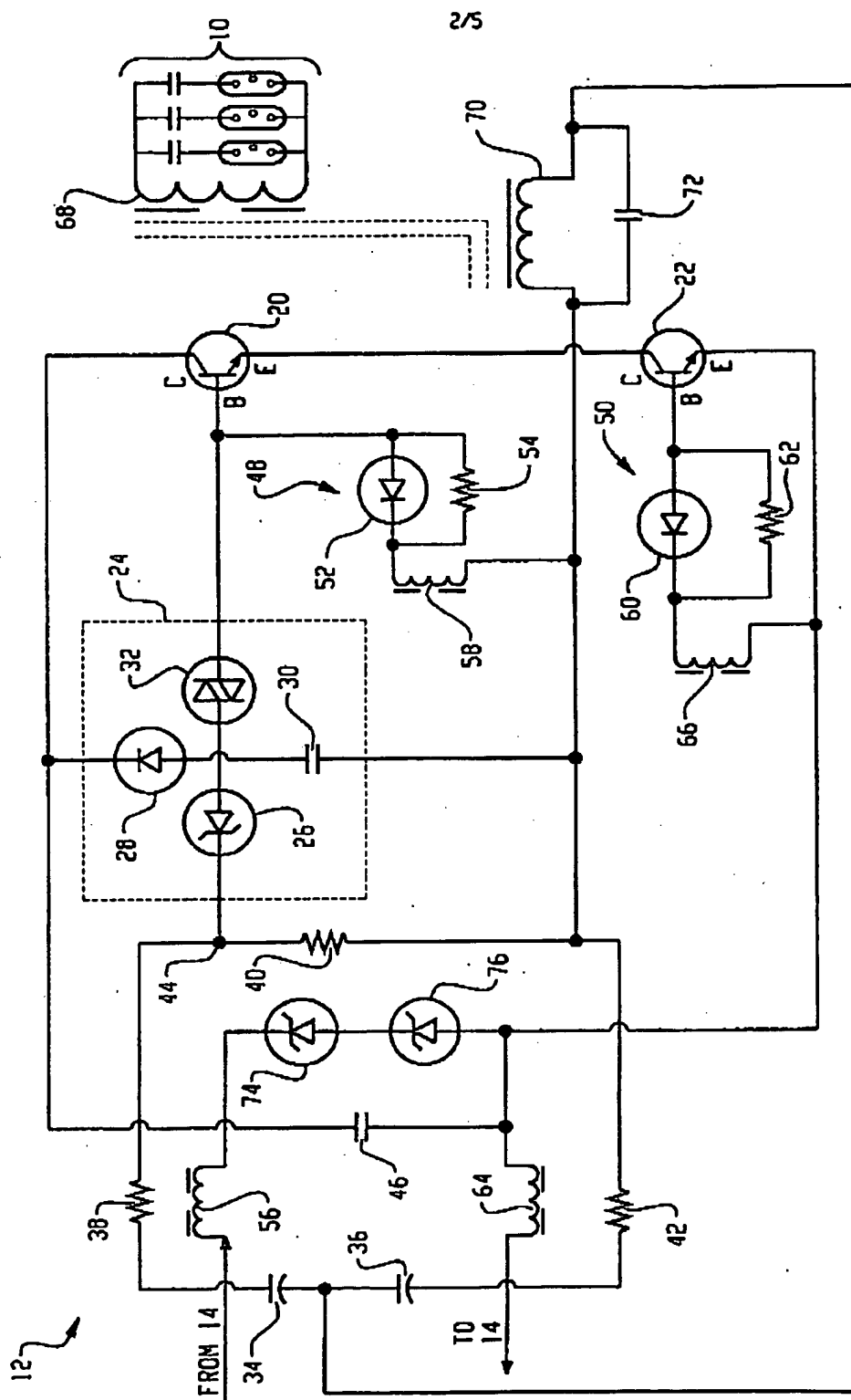
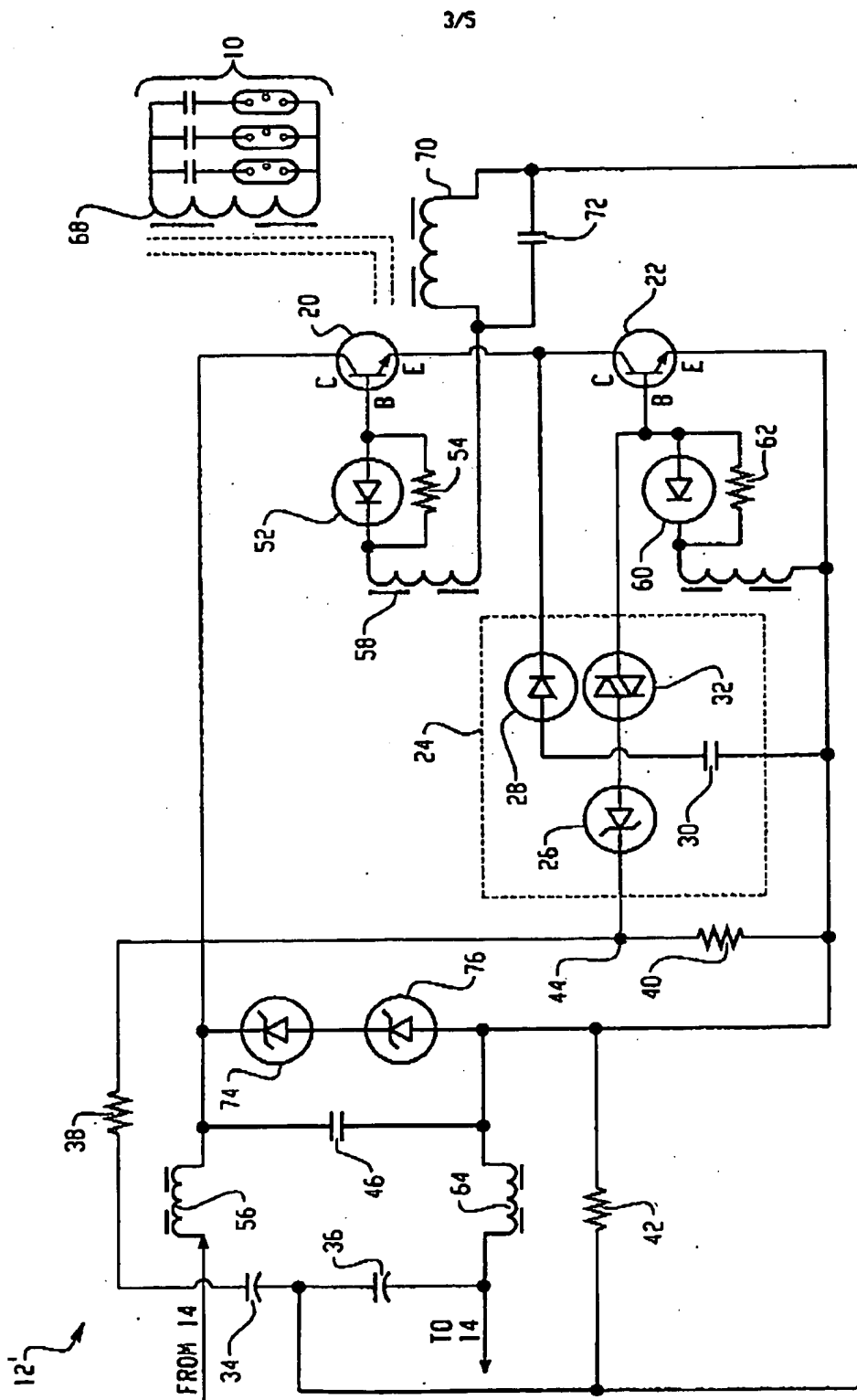


Fig. 2

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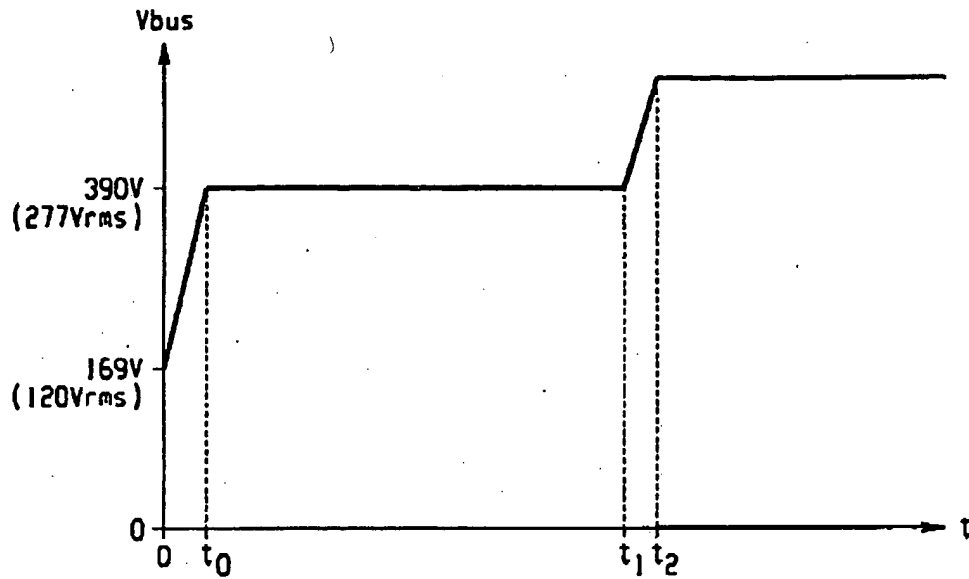


Fig. 4A

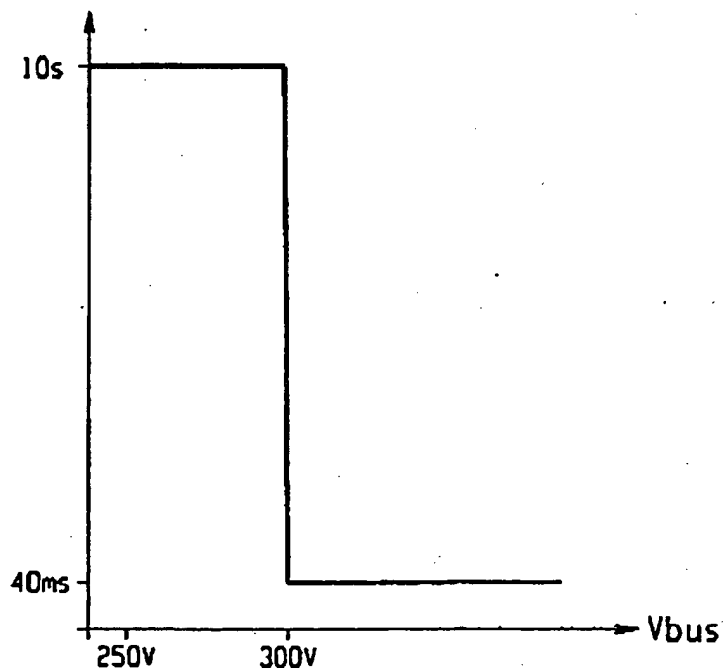


Fig. 4B

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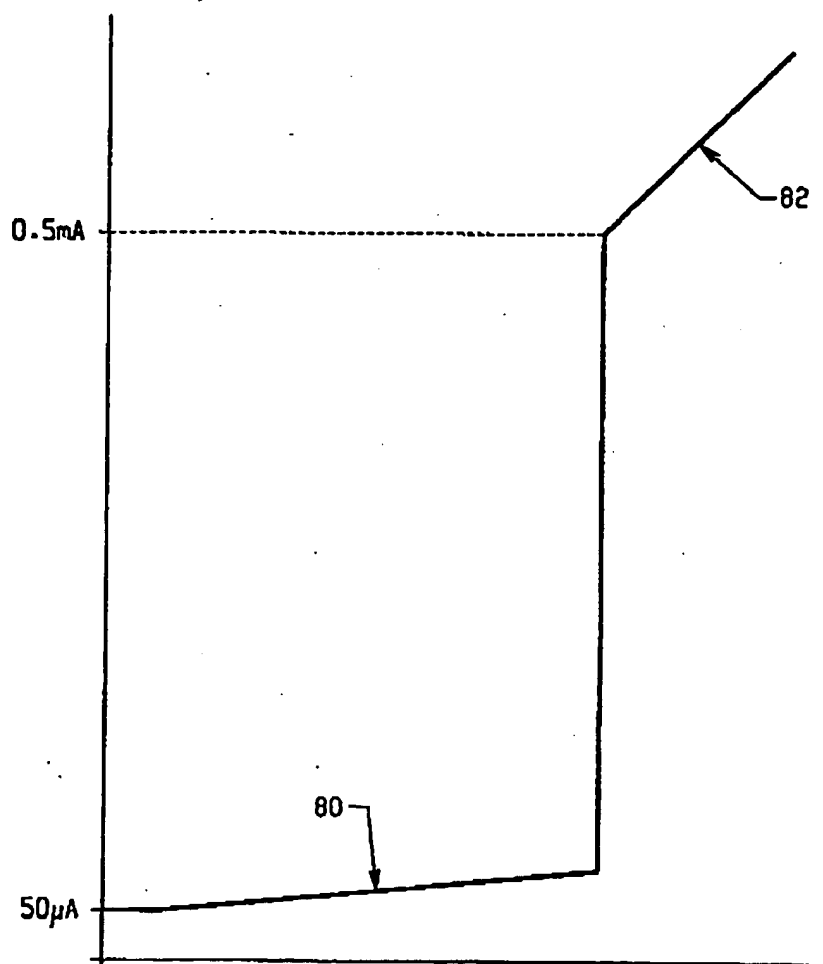


Fig. 5